**ANDERSON PUF**

**DETAILS OF ANDERSON PUF**:

I have tried to understand Anderson’s PUF Design model and have elaborated it’s working below for your review.

A screen shot of a computer

Description generated with very high confidence

Anderson’s Model of PUF Design tells that the components which are used in his design are fixed at a place and can be moved by changing it in the code. The code for it is given below.

attribute rloc\_range of SRL16E\_inst: label is "X0Y0:X52Y53"; -- region 1

attribute rloc\_range of SRL16E\_inst: label is "X0Y54:X52Y106"; -- region 2

attribute rloc\_range of SRL16E\_inst: label is "X0Y107:X52Y159"; -- region 3

attribute rloc\_range of SRL16E\_inst: label is "X56Y0:X104Y53"; -- region 4

attribute rloc\_range of SRL16E\_inst: label is "X56Y54:X104Y106"; -- region 5

attribute rloc\_range of SRL16E\_inst: label is "X56Y107:X104Y159"; -- region 6

As explained in his paper the 2 LUT’s are used as shift registers and given the input as

LUT A:0101010010101010(0\*5555)

LUT B:1010101010101010(0\*AAAA)

This is hardcoded in the code too which I am showing for your kind reference.

A screenshot of a computer

Description generated with very high confidence

**CONCLUSION**:

If I make the code to occupy certain logic elements in the Altera board that was implemented by Anderson in a synthesizable format then I could make it work in Altera boards too.